	Application No.	Applicant(s)
	10/781,830	JO, HIROAKI
Notice of Allowability	Examiner	Art Unit
	Henry N. Tran	2629
The MAILING DATE of this communication appe All claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RI of the Office or upon petition by the applicant. See 37 CFR 1.313	(OR REMAINS) CLOSED in this ap or other appropriate communication GHTS. This application is subject to	plication. If not included not will be mailed in due course. THIS
1. X This communication is responsive to the application filed 2	<u>/20/04</u> .	
2. 🔀 The allowed claim(s) is/are <u>1-35</u> .		
<ul> <li>3. Acknowledgment is made of a claim for foreign priority unapprint a) All b) Some* c) None of the:</li> <li>1. Certified copies of the priority documents have</li> <li>2. Certified copies of the priority documents have</li> </ul>	been received.	
3. Copies of the certified copies of the priority do	cuments have been received in this	national stage application from the
International Bureau (PCT Rule 17.2(a)).		
* Certified copies not received:		
Applicant has THREE MONTHS FROM THE "MAILING DATE" noted below. Failure to timely comply will result in ABANDONM THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.  4.   A SUBSTITUTE OATH OR DECLARATION must be subm	IENT of this application.	
INFORMAL PATENT APPLICATION (PTO-152) which give	es reason(s) why the oath or declara	ation is deficient.
5. CORRECTED DRAWINGS ( as "replacement sheets") mus	st be submitted.	
(a) Including changes required by the Notice of Draftspers	son's Patent Drawing Review (PTO-	-948) attached
1)  hereto or 2)  to Paper No./Mail Date	•	
(b) ☐ including changes required by the attached Examiner's Paper No./Mail Date		
Identifying indicia such as the application number (see 37 CFR 1 each sheet. Replacement sheet(s) should be labeled as such in the		
<ol> <li>DEPOSIT OF and/or INFORMATION about the depo- attached Examiner's comment regarding REQUIREMENT</li> </ol>	sit of BIOLOGICAL MATERIAL I FOR THE DEPOSIT OF BIOLOGIC	must be submitted. Note the AL MATERIAL.
Attachment(s) 1. ⊠ Notice of References Cited (PTO-892)	5. Notice of Informal F	Patent Application (PTO-152)
2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948)	6. Interview Summary	(PTO-413),
3. ☑ Information Disclosure Statements (PTO-1449 or PTO/SB/0	Paper No./Mail Da 08), 7. ⊠ Examiner's Amendo	ite ment/Comment
Paper No./Mail Date/of % 6/23/04 4. ☐ Examiner's Comment Regarding Requirement for Deposit	8. X Examiner's Stateme	ent of Reasons for Allowance
of Biological Material	9.	Henry N. Tom
		HENRY N. Tran \$/24/06 PRIMARY EXAMINER

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## **DETAILED ACTION**

This Application has been examined. The original claims 1-35 are pending. The examination results are as follows.

## Information Disclosure Statement

1. The examiner has considered the documents listed in forms PTO-1449 submitted with the Information Disclosure Statements (IDS) received 07/07/2005 and 06/23/2004 (see the attached form) PTO-1449).

## Allowable Subject Matter

- 2. Claims 1-35 are allowed.
- 3. The following is an examiner's statement of reasons for allowance:

The present invention is directed to an electronic circuit comprising a digital-to-analog conversion circuit having a current mirror for outputting an analog current for producing high quality EL images. Each of the independent claims 1, 4, 5, 8, 11, 12, 16, 19, 22, 23 and 27 requires the uniquely distinct features: a diode-connected first transistor provided with a first control terminal, a plurality of second transistors, a plurality of third transistors, and a fourth transistor, which are particularly arranged as claimed (see figure 5); whereas, the closest prior art, the Hanada et al. patent (U.S. Patent No. 6,992,647), and the Kimura patent (U.S. Patent No. 7,046,240) disclose conventional electronic circuits, each comprising a digital-to-analog conversion circuit having a current mirror for outputting an analog current for driving an EL display panel, which, either alone or in combination, fails to teach or suggest the claimed invention.

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Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

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## Conclusion

- 4. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. They are U.S. Patents Nos.: 6,972,526 issued to Abe, 6,963,336 issued to Kimura, 6,961,054 issued to Matsumoto, and 6,961,166 issued to Kasai, which teaches current generating circuits for driving EL display devices.
- 5. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Henry N. Tran whose telephone number is 571-272-7760. The examiner can normally be reached on M-F 8:00-4:30.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Bipin H. Shalwala can be reached on 571-272-7681. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated

information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

Henry N Tran Primary Examiner

Henry N. Tom

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HT #1 8/24/06